

TO. : _____

NO. : _____



APPROVAL SHEET

MULTILAYER CERAMIC CAPACITOR
Commercial Grade
(General Type)

Approved by customer : (signing or stamping here)

SAMWHA CAPACITOR CO., LTD.		
Writtern by	Checked by	Approved by
		

2020. 04. 08.



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Home page : www.samwha.com

< SPEC SUMMARY >

SAMWHA Part no.	CS2012X7R102K500NRA		
Type	General		
Item	Specification	Unit	Test methods and Conditions(Capacitance,IR)
Capacitance	1	nF	Testing Frequency : 1 ±0.1kHz Testing Voltage : 1 ±0.2Vrms
Capacitance Tolerance	± 10	%	
Dissipation Factor	Max. 2.5	%	
Insulation Resistance	More than 10,000	MΩ	Applied the rated voltage for 2 minutes of charging.
Chip Size	2.00 ±0.20	L (mm)	*Capacitance Tolerance Code --- page 1/9 *Chip size ----- page 2/9 *Characteristics & Test Method----- page 3/9~6/9
	1.25 ±0.15	W (mm)	
	0.60 ±0.10	T (mm)	

Enactment : March 27,1996	STANDARD	NO	SW - M - 04B
	MULTILAYER CERAMIC CAPACITOR Commercial Grade	Page	1 / 9

1. General Article

Application Range

These specifications refer to the "Multilayer Ceramic Capacitors "mainly used to the computer equipment, communication equipment.

***Caution : Industrial equipment / For the high reliability equipment / LED equipment / Etc.**
Please contact sales representatives or product engineers before using the products.
(For details, please refer Page 9)

2. General Code

(1) Type Designation

<u>CS</u>	<u>2012</u>	<u>X7R</u>	<u>102</u>	<u>K</u>	<u>500</u>	<u>N</u>	<u>R</u>	<u>A</u>
(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)

1) Multilayer Ceramic Capacitor (Commercial Grade)

2) Size Code :

This is expressed in tens of a millimeter.

The first two digits are the length, The last two digits are width.

3) Temperature Coefficient Code

Classification	Code	Temperature Range	Capacitance Tolerance
Class I	C0G	-55 to +125℃	±30 ppm/℃
Class II	X5R	-55 to +85℃	±15%
	X7R	-55 to +125℃	±15%
	Y5V	-30 to +85℃	+22% ~ -82%

4) Capacitance Code(Pico farads) :

The nominal Capacitance Value in pF is expressed by three digit numbers.

The first two digits represents significant figures and the last digit denotes the number of zero

ex) 104 = 100000 pF

R denotes decimal

8R2 = 8.2 pF

5) Capacitance Tolerance Code

Code	Tolerance
B	± 0.1 pF
C	± 0.25 pF
D	± 0.5 pF
F	± 1.0 %
G	± 2.0 %
J	± 5 %
K	± 10 %

Code	Tolerance
M	± 20 %
P	+ 100, - 0%
Z	+ 80, - 20%
H	+ 0.25/-0 pF
I	+ 0/-0.25 pF
U	+ 5/-0 %
V	+ 0/-5 %

6) Voltage Code

code	6R3	100	160	250	350	500	101	201	251	501	631	102	202	302
Vol.	DC 6.3V	DC 10V	DC 16V	DC 25V	DC 35V	DC 50V	DC 100V	DC 200V	DC 250V	DC 500V	DC 630V	DC 1KV	DC 2KV	DC 3KV

7) Termination Code

ex) N : Ni-Sn (Nickel-Tin Plate)

A : Ag/Ni-Sn (Ag Epoxy/Nickel-Tin Plate) -> **Soft Termination Type**

8) Packing Code

ex) R : 7" Reel Type

L : 13" Reel Type

B : Bulk Type

9) Thickness option

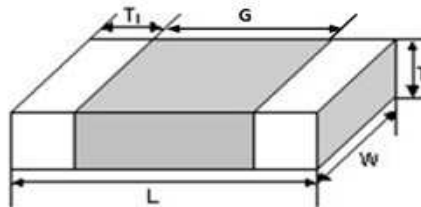
Thickness(mm)		Code	Thickness(mm)		Code
t	Tol(±)		t	Tol(±)	
0.30	0.03	Blank	1.30	0.20	E
0.50	0.05	Blank	1.35	0.20	H
0.60	0.10	A	1.60	0.20	I
0.80	0.10	B	1.80	0.20	J
0.85	0.15	B	2.00	0.25	K
1.00	0.15	E	2.50	0.25	L
1.10	0.15	E	2.80	0.30	M
1.15	0.15	E	3.20	0.30	N
1.25	0.15	E	5.00	0.40	O

3. Temperature Characteristics

See Page 6/9 (No.14)

4. Constructions and Dimensions

(I) Dimensions



(Unit : mm)

Code	Dimension					
	Length		Width		T1(min)	G(min)
	L	Tol(±)	W	Tol(±)		
0603	0.60	0.03	0.30	0.03	0.05	0.15
1005	1.00	0.05	0.50	0.05	0.05	0.30
1608	1.60	0.15	0.80	0.10	0.10	0.50
2012	2.00	0.20	1.25	0.15	0.10	0.65
3216	3.20	0.30	1.60	0.20	0.15	1.00
3225	3.20	0.40	2.50	0.25	0.15	1.05
4520	4.50	0.40	2.00	0.25	0.20	1.50
4532	4.50	0.40	3.20	0.30	0.20	1.50
5750	5.70	0.50	5.00	0.40	0.30	1.85

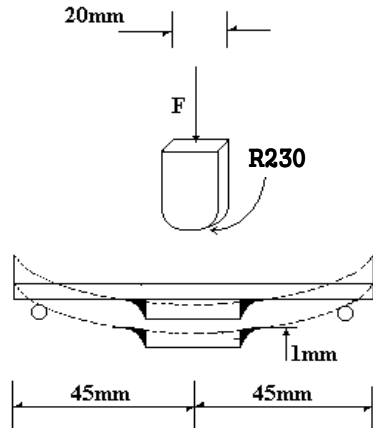
(2) Construction of Termination



Specifications and Test Methods (General)

No.	Item		Specification						Test Methods and Conditions															
			Class I		Class II																			
1	Operating Temperature Range		C0G : -55 to +125℃		X7R : -55 to +125℃ X5R : -55 to +85℃ Y5V : -30 to +85℃																			
2	Insulation Resistance		More than 10,000 MΩ or 500Ω.F (Whichever is smaller)						·Applied the rated voltage for 2 minutes of charging, The charge/discharge current is less than 50mA.															
3	Dielectric Strength		No defects or abnormalities						C0G : The rated voltage ×300% X7R, X5R, Y5V : " ×250% - Applied between the terminations for 1 to 5 seconds. - The charge/discharge current is less than 50mA.															
4	Capacitance		within the specified tolerance						The capacitance/Q/D.F. should be measured at 25℃ at the frequency and voltage shown in the table. <table><tr><th>Cap</th><th>Testing frequency</th><th>Testing Voltage</th></tr><tr><td>C0G (C<1000pF)</td><td>1±0.1MHz</td><td>0.5to5 Vrms</td></tr><tr><td>C0G (C≥1000pF)</td><td>1±0.1kHz</td><td>1±0.2 Vrms</td></tr><tr><td>X7R, X5R,Y5V (C≤10μF)</td><td>1±0.1kHz</td><td>1±0.2 Vrms</td></tr><tr><td>X7R, X5R, Y5V (C>10μF)</td><td>120±24Hz</td><td>0.5±0.1 Vrms</td></tr></table> · Initial measurement Perform the initial measurement according to Note1 for Class II · Measurement after test Take it out and set it for 24±2 hours (Class I) or 24±2 hours (Class II) then measure	Cap	Testing frequency	Testing Voltage	C0G (C<1000pF)	1±0.1MHz	0.5to5 Vrms	C0G (C≥1000pF)	1±0.1kHz	1±0.2 Vrms	X7R, X5R,Y5V (C≤10μF)	1±0.1kHz	1±0.2 Vrms	X7R, X5R, Y5V (C>10μF)	120±24Hz	0.5±0.1 Vrms
Cap	Testing frequency	Testing Voltage																						
C0G (C<1000pF)	1±0.1MHz	0.5to5 Vrms																						
C0G (C≥1000pF)	1±0.1kHz	1±0.2 Vrms																						
X7R, X5R,Y5V (C≤10μF)	1±0.1kHz	1±0.2 Vrms																						
X7R, X5R, Y5V (C>10μF)	120±24Hz	0.5±0.1 Vrms																						
5	Dissipation Factor		30pFmin : Q≥1,000(DF≤0.1%) 30pFmax : Q≥400+20C (DF≤1/ (400+20C))	Char.	50V min	25V	16V	10V	6.3V															
				X7R X5R	≤2.5%/ *≤5%	≤3%/ *≤7%	≤3.5%/ *≤7%	≤5%/ *≤10%	≤5%/ *≤10%															
				Y5V	≤5%/ *≤9%	≤7%/ *≤9%	≤9%/ *≤12.5%	≤12.5%/ *≤15%	≤15%															
6	Solderability of Termination		-Termination should be covered with more than 75% of new solder						*Pb-Free type Solder : 96.5Sn-3Ag-0.5Cu Solder temperature : 245±5℃ Immersion time : 3±0.1sec *Pre-Heating : at 80~120℃ for 10~30sec															
7	Resistance to Soldering Heat	Appearance	No defects which may affect performance						·Preheat the capacitor at 120 to 150℃ for 1 minute. (Preheating for 3225,4520,4532 Step1:100℃ to 120℃, 1min Step2:170℃ to 200℃, 1min) Immerse the capacitor in a eutectic solder solution ·Soldering Temp:260 ±5℃ ·Immersion Time:10 ±0.5 sec ·Initial measurement Perform the initial measurement according to Note1 for Class II ·Measurement after test Take it out and set it for 24±2 hours (Class I) or 24±2 hours (Class II) then measure															
		Capacitance change	within ±2.5% or ±0.25pF (whichever is larger)	X7R, X5R: ≤ ±7.5% Y5V : ≤ ±20%																				
		Dissipation Factor (or Q)	30pFmin : Q≥1,000 (DF≤0.1%) 30pFmax : Q≥400+20C (DF≤1/(400+20C))	Char.	50V min	25V	16V	10V		6.3V														
				X7R X5R	≤2.5%/ *≤5%	≤3%/ *≤7%	≤3.5%/ *≤7%	≤5%/ *≤10%		≤5%/ *≤10%														
			Y5V	≤5%/ *≤9%	≤7%/ *≤9%	≤9%/ *≤12.5%	≤12.5%/ *≤15%	≤15%																
		I.R.	More than 10,000MΩ or 500Ω.F (whichever is smaller)																					

No.	Item	Specification							Test Methods and Conditions																
		Class I	Class II																						
8	Temperature Cycle	Appearance	No defects which may affect performance							Perform the five cycles according to the four heat treatments listed in the following table. <table><tr><td>Step</td><td>1</td><td>2</td><td>3</td><td>4</td></tr><tr><td>Temp (°C)</td><td>Min. operating temp. +0, -3</td><td>Room Temp</td><td>Max. operating temp. +3, -0</td><td>Room Temp</td></tr><tr><td>Time (min)</td><td>30±3</td><td>2 to3</td><td>30±3</td><td>2 to3</td></tr></table> Initial measurement Perform the initial measurement according to Note1 for Class II Measurement after test Perform the final measurement according to Note2	Step	1	2	3	4	Temp (°C)	Min. operating temp. +0, -3	Room Temp	Max. operating temp. +3, -0	Room Temp	Time (min)	30±3	2 to3	30±3	2 to3
		Step	1	2	3	4																			
		Temp (°C)	Min. operating temp. +0, -3	Room Temp	Max. operating temp. +3, -0	Room Temp																			
		Time (min)	30±3	2 to3	30±3	2 to3																			
Capacitance Change	Within ±2.5% or ±0.25pF (whichever is larger)	X7R, X5R : Within ±7.5% Y5V : Within ±20%																							
Dissipation Factor (or Q)	30pFmin : Q≥1,000 (DF≤0.1%) 30pFmax : Q≥400+20C (DF≤1/(400+20C))	Char	50V min	25V	16V	10V	6.3V																		
		X7R X5R	≤5%/ *≤7.5%	≤5%/ *≤10%	≤5%/ *≤10%	≤7.5%/ *≤12.5	≤7.5%/ *≤12.5%																		
		Y5V	≤7.5%/ *≤12.5%	≤10%/ *≤12.5%	≤12.5%/ *≤15%	≤15%/ *≤20%	≤20%																		
I.R	More than 10,000MΩ or 500Ω.F (Whichever is smaller)																								
9	Humidity Load	Appearance	No defects which may affect performance							Temperature : 40±2°C Humidity : 90~95%RH Hour : 500+24/-0 hrs Applied Voltage : Rated Voltage The charge/discharge current is less than 50mA. Initial measurement Perform the initial measurement according to Note1 for Class II Measurement after test Perform the final measurement according to Note2															
		Capacitance Change	Within ±7.5% or ±0.75pF (whichever is larger)	X7R, X5R : Within ±12.5% Y5V : Within +30%, -40% (Y5V/1.0μF, 2.2μF, 4.7μF/10V) Within ±30% (others)																					
		Dissipation Factor(or Q)	30pFmin : Q≥200 (DF≤0.5%) 30pFmax : Q≥100+10/3C DF≤1/(100+10/3C)	Char	50V min	25V	16V	10V	6.3V																
				X7R X5R	≤5%/ *≤7.5%	≤5%/ *≤10%	≤5%/ *≤10%	≤7.5%/ *≤12.5	≤7.5%/ *≤12.5%																
Y5V	≤7.5%/ *≤12.5%			≤10%/ *≤12.5%	≤12.5%/ *≤15%	≤15%/ *≤20%	≤20%																		
I.R	More than 500MΩ or 25Ω.F (Whichever is smaller)																								
10	High Temperature Load	Appearance	No defects which may affect performance							Testing time : 1000+48/-0 hrs Applied voltage : Rated voltage < DC250V : ×200% Temperature : C0G, X7R → 125±3°C X5R, Y5V → 85±3°C The charge/discharge current is less than 50mA. Initial measurement Perform the initial measurement according to Note1 for Class II Measurement after test Perform the final measurement according to Note2															
		Capacitance Change	Within ±3% or ±0.3pF (whichever is larger)	X7R, X5R : Within ±12.5% Y5V : Within ±30% (cap<1.0μF) Within +30%, -40% (cap≥1.0μF)																					
		Dissipation Factor (or Q)	30pFmin : Q≥350 (DF≤0.3%) 10pF≤Cp≤30pF : Q≥275+5/2C (DF≤1/(275+5/2C)) 10pFmax : Q≥200+10C (DF≤1/(200+10C))	Char	50V min	25V	16V	10V	6.3V																
				X7R X5R	≤5%/ *≤7.5%	≤5%/ *≤10%	≤5%/ *≤10%	≤7.5%/ *≤12.5	≤7.5%/ *≤12.5%																
Y5V	≤7.5%/ *≤12.5%			≤10%/ *≤12.5%	≤12.5%/ *≤15%	≤15%/ *≤20%	≤20%																		
I.R	More than 1,000MΩ or 50Ω.F (whichever & smaller)																								

No.		Item	Specification				Test Methods and Conditions																			
			Class I		Class II																					
11	Bending strength		 No cracking or marking defects shall occur						·Substrate material : Glass EPOXY Board. ·Thickness : 1.6mm 0.8mm(0603/1005size) *. Test condition – Bending limit : 1mm – Pressurizing speed : 1mm/sec – Holding time : 5±1sec																	
		Capacitance Change	Within ±5% or ±0.5pF (whichecker is larger)	X7R, X5R: Within ±12.5% Y5V: Within ±30%																						
12	Vibration Resistance	Appearance	No defects or abnormalities																							
		Capacitance	Whin the specified tolerance																							
		Q/DF	30pFmin : Q≥1,000 (DF≤0.1%) 30pFmax : Q≥400+20C (DF≤1/ (400+20C))	<table><tr><td>Char.</td><td>50V min</td><td>25V</td><td>16V</td><td>10V</td><td>6.3V</td></tr><tr><td>X7R X5R</td><td>≤2.5%/ *≤5%</td><td>≤3%/ *≤7%</td><td>≤3.5%/ *≤7%</td><td>≤5%/ *≤10%</td><td>≤5%/ *≤10%</td></tr><tr><td>Y5V</td><td>≤5%/ *≤9%</td><td>≤7%/ *≤9%</td><td>≤9%/ *≤12.5%</td><td>≤12.5%/ *≤15%</td><td>≤15%</td></tr></table>			Char.	50V min	25V	16V	10V	6.3V	X7R X5R	≤2.5%/ *≤5%	≤3%/ *≤7%	≤3.5%/ *≤7%	≤5%/ *≤10%	≤5%/ *≤10%	Y5V	≤5%/ *≤9%	≤7%/ *≤9%	≤9%/ *≤12.5%	≤12.5%/ *≤15%	≤15%		
Char.	50V min	25V	16V	10V	6.3V																					
X7R X5R	≤2.5%/ *≤5%	≤3%/ *≤7%	≤3.5%/ *≤7%	≤5%/ *≤10%	≤5%/ *≤10%																					
Y5V	≤5%/ *≤9%	≤7%/ *≤9%	≤9%/ *≤12.5%	≤12.5%/ *≤15%	≤15%																					
13	Humidity Steady State	Appearance	No defects which may affect performance				·Temperature : 40±2℃ ·Humidity : 90~95%RH ·Hour : 500+24/-0 hrs ·Initial measurement Perform the initial measurement according to Note1 for Class II ·Measurement after test Perform the final measurement according to Note2																			
		Capacitance Change	within ±5% or ±0.5pF (Whichecker is larger)	X7R, X5R: Within ±12.5% Y5V: Within ±30%																						
		Dissipation (or Q)	30pF min : Q≥350 (DF≤0.3%) 10pF≤Cp≤30pF : Q≥275+5/2C (DF≤1/(275+5/2C)) 10pFmax : Q≥200+10C (DF≤1/(200+10C))	<table><tr><td>Char</td><td>50V min</td><td>25V</td><td>16V</td><td>10V</td><td>6.3V</td></tr><tr><td>X7R X5R</td><td>≤5%/ *≤7.5%</td><td>≤5%/ *≤10%</td><td>≤5%/ *≤10%</td><td>≤7.5%/ *≤12.5%</td><td>≤7.5%/ *≤12.5%</td></tr><tr><td>Y5V</td><td>≤7.5%/ *≤12.5%</td><td>≤10%/ *≤12.5%</td><td>≤12.5%/ *≤15%</td><td>≤15%/ *≤20%</td><td>≤20%</td></tr></table>				Char	50V min	25V	16V	10V	6.3V	X7R X5R	≤5%/ *≤7.5%	≤5%/ *≤10%	≤5%/ *≤10%	≤7.5%/ *≤12.5%	≤7.5%/ *≤12.5%	Y5V	≤7.5%/ *≤12.5%	≤10%/ *≤12.5%	≤12.5%/ *≤15%	≤15%/ *≤20%	≤20%	
		Char	50V min	25V	16V	10V		6.3V																		
X7R X5R	≤5%/ *≤7.5%	≤5%/ *≤10%	≤5%/ *≤10%	≤7.5%/ *≤12.5%	≤7.5%/ *≤12.5%																					
Y5V	≤7.5%/ *≤12.5%	≤10%/ *≤12.5%	≤12.5%/ *≤15%	≤15%/ *≤20%	≤20%																					
I.R.	More than 1,000MΩ or 50Ω.F (Whichecker is smaller)																									

No.	Item		Specification				Test Methods and Conditions																										
			Class I	Class II																													
14	Capacitance Temperature Characteristics	Capacitance Change	<table><tr><td>Char.</td><td>Temp Range</td><td>Reference Temp.</td><td>Cap Change</td></tr><tr><td>X7R</td><td>-55 to +125℃</td><td rowspan="3">25℃</td><td>Within ±15%</td></tr><tr><td>X5R</td><td>-55 to +85℃</td><td>Within ±15%</td></tr><tr><td>Y5V</td><td>-30 to +85℃</td><td>Within +22% -82%</td></tr></table>				Char.	Temp Range	Reference Temp.	Cap Change	X7R	-55 to +125℃	25℃	Within ±15%	X5R	-55 to +85℃	Within ±15%	Y5V	-30 to +85℃	Within +22% -82%	(1) Temperature Compensating Type The temperature coefficient is determined using the capacitance measured in step 3 as a reference. When cycling the temperature sequentially from step 1 through 5,(C0G: +25 to 125℃) the capacitance shall be with in the specified tolerance for the temperature coefficient. The capacitance drift is calculated by dividing the difference between the maximum measured values in the step 1, 3 and 5 by the cap. value in step 3 <table><tr><th>Step</th><th>Temperature(℃)</th></tr><tr><td>1</td><td>25±2</td></tr><tr><td>2</td><td>-55±3</td></tr><tr><td>3</td><td>25±2</td></tr><tr><td>4</td><td>125±3(for C0G)</td></tr><tr><td>5</td><td>25±2</td></tr></table>	Step	Temperature(℃)	1	25±2	2	-55±3	3	25±2	4	125±3(for C0G)	5	25±2
		Char.	Temp Range	Reference Temp.	Cap Change																												
X7R	-55 to +125℃	25℃	Within ±15%																														
X5R	-55 to +85℃		Within ±15%																														
Y5V	-30 to +85℃		Within +22% -82%																														
Step	Temperature(℃)																																
1	25±2																																
2	-55±3																																
3	25±2																																
4	125±3(for C0G)																																
5	25±2																																
		Temperature Coefficient	<table><tr><td>Char.</td><td>Temp Range</td><td>Temperature Coefficient</td></tr><tr><td>C0G</td><td>-55 to +125℃</td><td>±30 ppm/℃</td></tr></table>				Char.	Temp Range	Temperature Coefficient	C0G	-55 to +125℃	±30 ppm/℃	(2) High Dielectric Constant Type The ranges of capacitance change compared with the 25℃ value over the temperature range shown in the table shall be in the specified range.																				
Char.	Temp Range	Temperature Coefficient																															
C0G	-55 to +125℃	±30 ppm/℃																															

*Note1. Initial Measurement for Class II

Perform a heat treatment at 150+0,-10℃ for one hour and then let sit for 24±2 hours at room temperature, then measure

*Note2. Measurement after test

1. Class I

Let sit for 24±2 hours at room temperature, then measurement

2. Class II

Perform a heat treatment at 150+0,-10℃ for one hour and then let sit for 24±2 hours at room temperature, then measure.

5. Packing

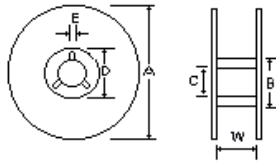
(1) Bulk packing

- ① 1000 pcs per Polybag
- ② 5 Polybags per Inner box
- ③ 10 Inner boxes per Out box

(2) Reel Packing

- ① 8~10 Reels per Inner box
- ② 6 Inner boxes per Out box

(3) Reel Dimensions



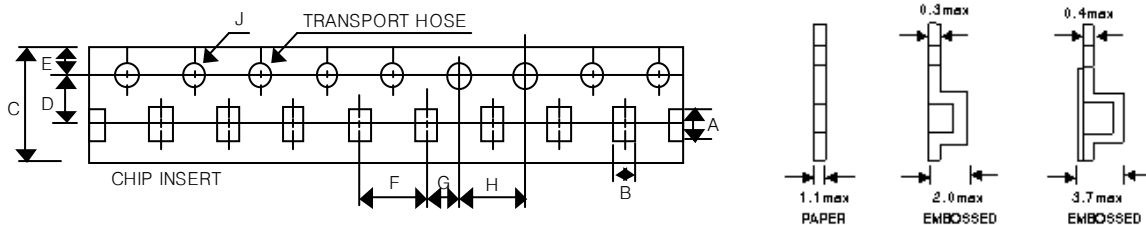
(Unit : mm)

MARK	SIZE	A	B	C	D	E	W
7 " REEL	0603~3225	$\Phi 178 \pm 2$	$\Phi 50 \text{Min}$	$\Phi 13 \pm 0.5$	$\Phi 21 \pm 0.8$	2 ± 0.5	10 ± 1.5
	4520~4532	$\Phi 180 +0, -3$	$\Phi 60 -0, +1$	$\Phi 13 \pm 0.2$	$\Phi 57 -0 +1$	3 ± 0.2	13 ± 0.5
13 " REEL	1005~3225	$\Phi 330 \pm 2$	$\Phi 70 \text{Min}$	$\Phi 13 \pm 0.5$	$\Phi 21 \pm 0.8$	2 ± 0.5	10 ± 1.5

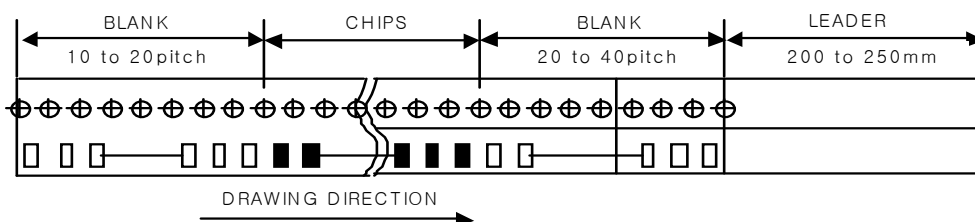
(4) Number of Package

TYPE	EIA CODE	7"	13"
		Qt/REEL	Qt/REEL
CS0603	CC0201	15,000	
CS1005	CC0402	10,000	50,000
CS1608	CC0603	4,000	15,000
CS2012	CC0805	3,000 ~ 4,000	8,000 ~ 15,000
CS3216	CC1206	2,000 ~ 4,000	6,000 ~ 10,000
CS3225	CC1210	1,000 ~ 3,000	4,000 ~ 10,000
CS4520	CC1808	1,500 ~ 3,000	-
CS4532	CC1812	500 ~ 1,000	1,500 ~ 5,000

(5) Tape Dimensions



TYPE	EIA CODE	A	B	C	D	E	F	G	H	J
CS0603	CC0201	0.67 ± 0.05	0.37 ± 0.05	8.0 ± 0.3	3.5 ± 0.05	1.75 ± 0.1	2.0 ± 0.05	2.0 ± 0.1	4.0 ± 0.1	1.5 ± 0.1
CS1005	CC0402	1.15 ± 0.1	0.65 ± 0.1	8.0 ± 0.3	3.5 ± 0.05	1.75 ± 0.1	2.0 ± 0.05	2.0 ± 0.1	4.0 ± 0.1	1.5 ± 0.1
CS1608	CC0603	1.9 ± 0.2	1.10 ± 0.2	8.0 ± 0.3	3.5 ± 0.05	1.75 ± 0.1	4.0 ± 0.1	2.0 ± 0.1	4.0 ± 0.1	1.5 ± 0.1
CS2012	CC0805	2.4 ± 0.2	1.65 ± 0.2	8.0 ± 0.3	3.5 ± 0.05	1.75 ± 0.1	4.0 ± 0.1	2.0 ± 0.1	4.0 ± 0.1	1.5 ± 0.1
CS3216	CC1206	3.6 ± 0.2	2.00 ± 0.2	8.0 ± 0.3	3.5 ± 0.05	1.75 ± 0.1	4.0 ± 0.1	2.0 ± 0.1	4.0 ± 0.1	1.5 ± 0.1
CS3225	CC1210	3.6 ± 0.2	2.80 ± 0.2	8.0 ± 0.3	3.5 ± 0.05	1.75 ± 0.1	4.0 ± 0.1	2.0 ± 0.1	4.0 ± 0.1	1.5 ± 0.1
CS4520	CC1808	4.8 ± 0.2	2.3 ± 0.2	12.0 ± 0.3	5.5 ± 0.1	1.75 ± 0.1	4.0 ± 0.1 8.0 ± 0.1	2.0 ± 0.1	4.0 ± 0.1	1.5 ± 0.1
CS4532	CC1812	4.9 ± 0.2	3.6 ± 0.2	12.0 ± 0.3	5.5 ± 0.1	1.75 ± 0.1	8.0 ± 0.1	2.0 ± 0.1	4.0 ± 0.1	1.5 ± 0.1



6. Caution

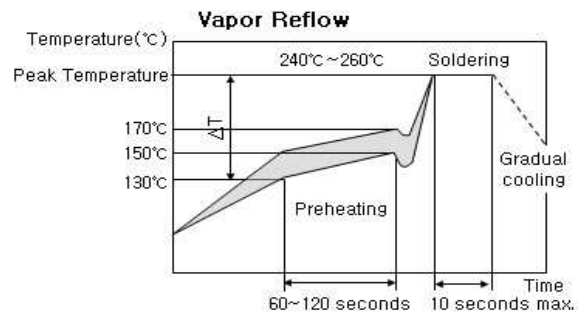
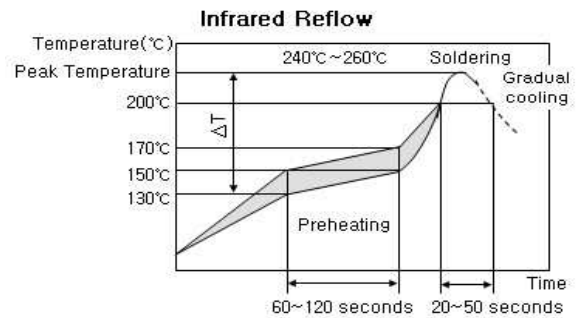
► Reflow Soldering

1. The sudden temperature change easily causes mechanical damages to ceramic components. Therefore, the preheating procedures should be required for the soldering of ceramic components.
2. Please refer to the recommended soldering profiles as shown in figures, and keep the temperature difference(ΔT) within the range recommended in Table 1.

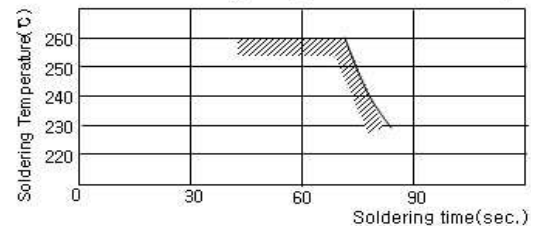
Table 1

Size code	Temperature Difference
0603, 1005, 1608, 2012, 3216	$\Delta T \leq 190^\circ\text{C}$
3225size and over	$\Delta T \leq 130^\circ\text{C}$

[Standard Conditions for Reflow Soldering]



[Allowable Soldering Temperature and Time]



In case of repeated soldering, the accumulated soldering time must be within the range shown above.

► Storage Condition

*When Solderability is considered, Capacitor are recommended to be used in 12 months

- (1) Temperature: $25^\circ\text{C} \pm 10^\circ\text{C}$
- (2) Relative Humidity: Below 70% RH

► The Regulation of Environmental Pollution Materials.

*Never use materials mentioned below in MLCC products regulated this document.

Pb, Cd, Hg, Cr^{+6} , PBB(Polybromide biphenyl), PBDE(Polybrominated diphenyl ethers), asbestos.

* Note

(1) 'Aging'/'De-aging' Behavior of high dielectric MLCCs

(Typically represented by X7R, Y5V temperature characteristic of which main composition is BaTiO₃)

'Aging' / 'De-aging' Behavior of high dielectric MLCCs Please note that high dielectric type dielectric Ceramic Capacitors have a "normal" 'aging' behavior / characteristic, that is; their capacitance value decreases with time from its value when it was first manufactured. From that date, the capacitance value begins to decrease at a logarithmic rate defined by:

$$C_t = C_{24} (1 - k \log_{10} t)$$

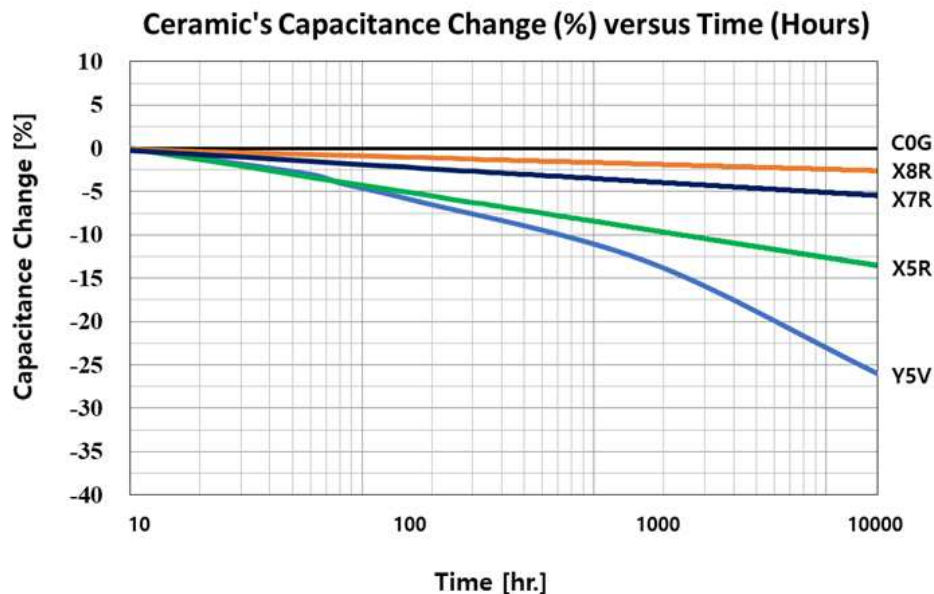
where :

C_t = Capacitance Value, t hours after the start of 'aging'

C_{24} = Capacitance Value, 24 hours after its manufacture

k = aging constant (capacitance decrease per decade-hour)

t = time, in hours, from the start of 'aging'



The capacitance value can be restored (a.k.a. 'de-aged') by exposing the component to elevated temperatures approaching its Curie Temperature (approximately 120°C). This 'deaging' can occur during the component's solder-assembly onto the PCB, during life or temperature cycle testing., or by ' baking ' at 150°C for about 1 hour.

- (2) Please contact our sales representatives or product engineers before using the products in this catalog for the applications listed below, which require especially high reliability for the prevention of defects which might directly damage a third party's life, body or property, or when one of our products is intended for use in applications other than those specified in this catalog.

- ① Aircraft equipment ② Aerospace equipment ③ Undersea equipment ④ Power plant equipment
- ⑤ Medical equipment ⑥ Transportation equipment (vehicles, trains, ships, etc.)
- ⑦ Traffic signal equipment ⑧ Disaster prevention / crime prevention equipment
- ⑨ Industrial equipment (Conveyors, Robot equipment, etc) ⑩ Led equipment
- ⑪ Application of similar complexity and/or reliability requirements to the applications listed above