

Specification for Approval

Date: 2025/09/05

Customer : Blume

TAI-TECH P/N: HPC5040NV-220M

CUSTOMER P/N:

DESCRIPTION:

QUANTITY: pcs

REMARK:

Customer Approval Feedback

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APPROVED	CHECKED
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APPROVED	CHECKED	DRAWN
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ECN HISTORY LIST					
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	25/09/05	New Issue	Hs Chen	Mr.Liang	Xu yaoyao
備 注					

SMD Power Inductor

1. Features

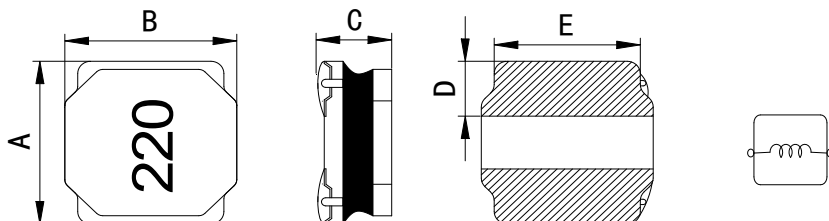
1. This specification applies Low Profile Power Inductors.
2. 100% Lead(Pb)-Free & Halogen-Free and RoHS compliant.
3. High reliability -Reliability tests comply with AEC-Q200
4. Operating temperature: -55~+125℃ (Including self - temperature rise)



2. Applications

Automotive applications.

3. Dimension

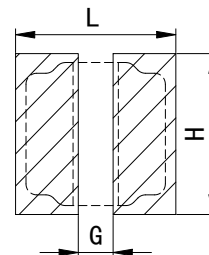


Inductance	A	B	C	D	E
≤10 uH	4.95±0.2	4.95±0.2	3.9±0.2	1.3±0.3	4.2±0.5
>10 uH			3.8±0.2		

Unit:mm

- Note: 1. A, B Size may slightly bigger than ferrite core dimension after epoxy sealing, but not exceed 0.1mm.
 2. Component height may slight higher than C size and not exceed 0.1mm, and will be lower after reflow soldering

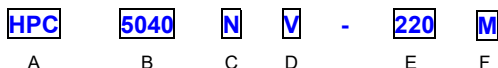
Recommend Land pattern



L	G	H
5.5	1.8	5.5

- Note: 1. PCB layout is referred to standard IPC-7351B
 2. The above PCB layout reference only.
 3. Recommend solder paste thickness at 0.12mm and above.

4. Part Numbering



A: Series

B: Dimension

C: Type

D: Category Code

E: Inductance

F: Inductance Tolerance

A/B*C

V=Vehicle

220=22.0uH

K=±10%, L=±15%, M=±20%, Y=±30%.

marking direction cannot decide polarity. Color: Black, unidirectional.
 magnetic shielding

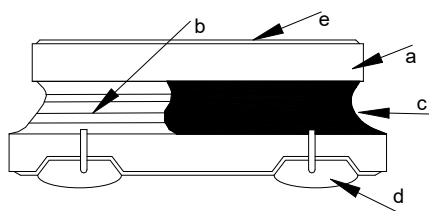
5. Specification

Part Number	Inductance (uH) ±20% @ 0 A DC	I rms (A)	I sat (A)	DCR (mΩ) ±20%
		Typ	Typ	
HPC5040NV-220M	22.0	1.50	1.62	123

Note:

1. All test data referenced to 25℃ ambient, Ls:100KHz/1V.
2. Testing Instrument: HP4284A, CH11025, CH3302, CH1320, CH1320S LCR METER / Rdc: CH502BC MICRO OHMMETER.
3. Heat Rated Current (Irms) will cause the coil temperature rise approximately ΔT of 40℃
4. Saturation Current (Isat) will cause L0 to drop approximately 30%.
5. The part temperature (ambient + temp rise) should not exceed 125℃ under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
6. Special inquiries besides the above common used types can be met on your requirement.
7. Rated DC current: The lower value of I rms and Isat.

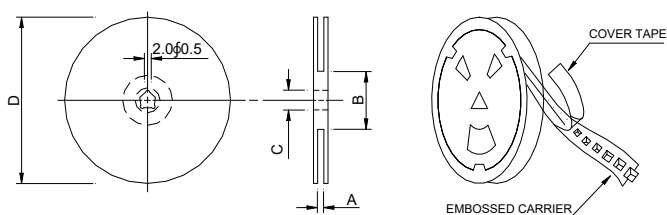
6. Material List



NO	Items	Materials
a	Core	Ferrite Core
b	Wire	Enameled Copper Wire
c	Glue	Epoxy with magnetic powder
d	Terminal	Ag/Ni/Sn + Sn Solder
e	Ink	Halogen-free ketone

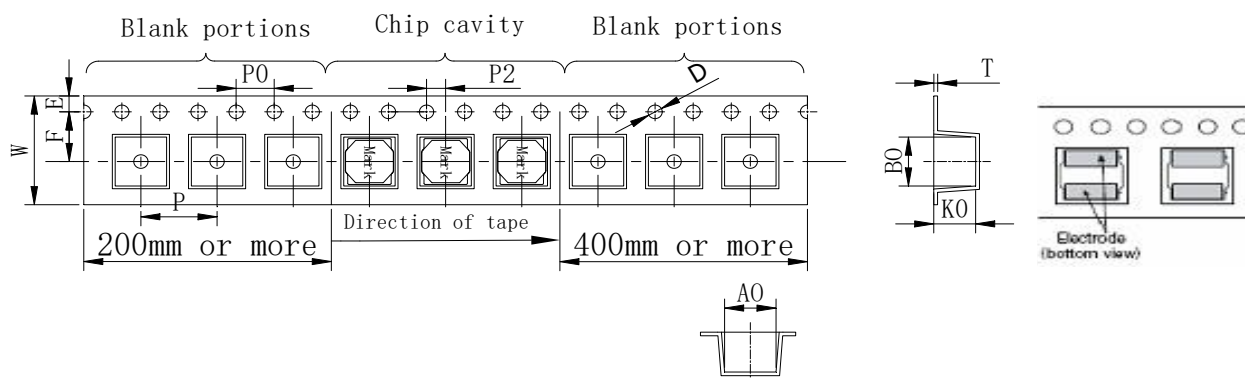
7. Packaging Information

(1) Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
13"x12mm	12.4+2/-0	80±2.0	13+0.5/-0.2	330±3.0

(2) Tape Dimension



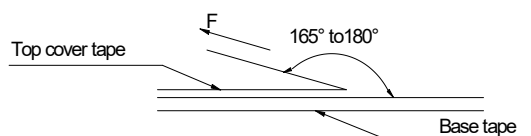
B0	A0	K0	P	W	T	E	F	D	P0	P2
5.4±0.1	5.4±0.1	4.3±0.1	8.0±0.1	12±0.3	0.4±0.1	1.75±0.1	5.5±0.1	1.5±0.1	4.0±0.1	2.00±0.1

Unit: mm

(3) Packaging Quantity

HPC	5040
Reel	1500

(4) Tearing Off Force



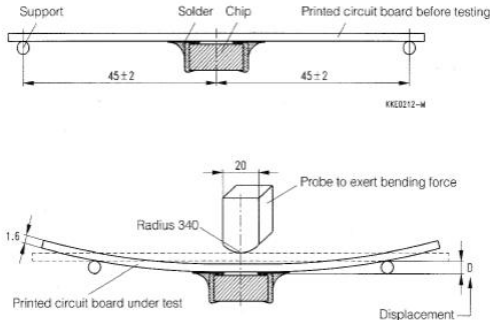
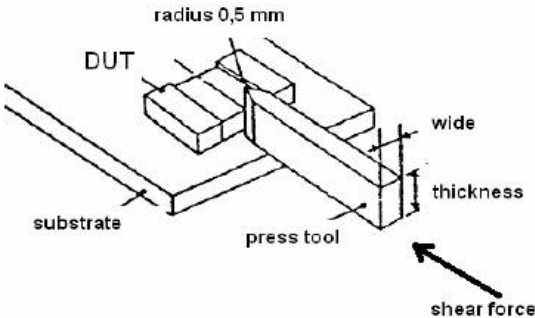
The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 standard).

Tearing Speed mm	Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)
300±10%	5~35	45~85	860~1060

8. Reliability and Test Condition

Item		Performance	Test Condition																												
Operating temperature		-55~+125℃(Including self - temperature rise)	NA																												
Storage temperature and Humidity range		1. -10~+40℃, 50~60%RH (Product with taping) 2. -55~+125℃(on board)	NA																												
Electrical Performance Test																															
Inductance		Refer to standard electrical characteristics list.	HP4284A, CH11025, CH3302, CH1320, CH1320S LCR Meter.																												
DCR			CH16502, Agilent33420A Micro-Ohm Meter.																												
Saturation Current (Isat)		Approximately ΔL30%	Saturation DC Current (Isat) will cause L0 to drop ΔL(%).																												
Heat Rated Current (Irms)		Approximately ΔT40℃	Heat Rated Current (Irms) will cause the coil temperature rise ΔT(℃). 1. Applied the allowed DC current. 2. Temperature measured by digital surface thermometer.																												
Reliability Test (For AEC-Q200-E)																															
8.1	Pre- and Post-Stress Electrical Test	User Specification	Test is performed at room temperature except as specified in the applicable stress reference and the additional requirements in this Table (e.g. 8.3, 8.4, 8.5) Preconditioning : run through reflow for 3 times. (IPC/JEDECJ-STD-020F Classification Reflow Profiles)																												
8.2	High Temperature Exposure(Storage) Reference MIL-STD-202 Method 108	Appearance : no damage. Inductance : within±10% of initial value. RDC : within±15% of initial value and shall not exceed the specification value.	Unpowered, Temperature : 125±2℃ Upper Temperature: maximum specified operating temperature or maximum specified storage temperature (whichever is higher). Duration : 1000hrs Min. Measured at room temperature after placing for 24±4 hrs.																												
8.3	Temperature Cycling Reference JESD22-A104		Conduct Temperature Cycling testing on the product after performing the Pre- and Post-Stress Electrical Tests as specified in section 8.1 Unpowered Lower Temperature of the Chamber: -40℃(For Inductors/Transformers) Upper Temperature of the Chamber: maximum specified operating temperature and shall not exceed 125℃ Condition for 1 cycle Step1 : -40±2℃ 30min Min. Step2 : 125±2℃ transition time 1min MAX Step3 : 125±2℃ 30min Min. Step4 : Dwell Time (Soak Time) 15 minutes minimum, 30 minutes minimum if component weighs above 28g Transition Time : 1 minute maximum Number of cycles : 1000 Measured at room temperature at least 24 hours after test conclusion.																												
8.4	Humidity Bias Reference MIL-STD-202 Method 103		Conduct Humidity Bias testing on the product after performing the Pre- and Post-Stress Electrical Tests as specified in section 8.1 Unpowered(For Inductors/Transformers) Humidity : 85±3% R.H, Temperature : 85℃±2℃ Duration : 1000hrs Min. Measured at room temperature after placing for 24±4hrs.																												
8.5	High Temperature Operating Life Reference MIL-STD-202 Method 108		Conduct High Temperature Operating Life testing on the product after performing the Pre- and Post-Stress Electrical Tests as specified in section 8.1 Temperature : 85±2℃ Upper Temperature of the Chamber: maximum specified operating temperature (not including heat rise) at maximum rated power and shall not exceed 125℃. (For Inductors/Transformers) Duration : 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24±4 hrs.																												
8.6	External Visual Reference MIL-STD-883 Method 2009		Inspect device construction, marking and workmanship. Pre and Post Electrical Test not required.																												
8.7	Physical Dimension Reference JESD22-B100	According to the product specification size measurement.	Verify physical dimensions to the applicable component detail specification. Pre and Post Electrical Test not required.																												
8.8	Terminal Strength (for axial and radial THT components) Reference MIL-STD-202 Method 211	Appearance : no damage. Inductance : within±10% of initial value. RDC : within±15% of initial value and shall not exceed the specification value.	Test THT component lead integrity only. Test Condition A (pull test) <table><tr><th>Nominal cross-sectional area(mm²)</th><th>Force (N)</th></tr><tr><td>≤0.05</td><td>1</td></tr><tr><td>0.06 to 0.10</td><td>2.5</td></tr><tr><td>0.11 to 0.20</td><td>5</td></tr><tr><td>0.21 to 0.50</td><td>10</td></tr><tr><td>0.51 to 1.20</td><td>20</td></tr><tr><td>≥1.20</td><td>40</td></tr></table> Test Condition C (wire-lead bend test) : <table><tr><th>Section Modulus (Zx) (mm³)</th><th>Force (N)</th></tr><tr><td>≤1.5x10⁻³</td><td>0.5</td></tr><tr><td>1.6x10⁻³ to 4.2x10⁻³</td><td>1.25</td></tr><tr><td>4.3x10⁻³ to 1.2x10⁻²</td><td>2.5</td></tr><tr><td>1.3x10⁻² to 0.5x10⁻¹</td><td>5</td></tr><tr><td>0.6x10⁻¹ to 1.9x10⁻¹</td><td>10</td></tr><tr><td>> 1.9x10⁻¹</td><td>20</td></tr></table> For round terminations : ZX = (πd³)/32 where d is the lead diameter. For strip terminations : ZX = (ba²)/6 where a is the thickness of the rectangular strip perpendicular to the bending axis, b is the other dimension of the rectangular strip	Nominal cross-sectional area(mm²)	Force (N)	≤0.05	1	0.06 to 0.10	2.5	0.11 to 0.20	5	0.21 to 0.50	10	0.51 to 1.20	20	≥1.20	40	Section Modulus (Zx) (mm³)	Force (N)	≤1.5x10 ⁻³	0.5	1.6x10 ⁻³ to 4.2x10 ⁻³	1.25	4.3x10 ⁻³ to 1.2x10 ⁻²	2.5	1.3x10 ⁻² to 0.5x10 ⁻¹	5	0.6x10 ⁻¹ to 1.9x10 ⁻¹	10	> 1.9x10 ⁻¹	20
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4.3x10 ⁻³ to 1.2x10 ⁻²	2.5																														
1.3x10 ⁻² to 0.5x10 ⁻¹	5																														
0.6x10 ⁻¹ to 1.9x10 ⁻¹	10																														
> 1.9x10 ⁻¹	20																														

Item		Performance	Test Condition																																														
8.9	Resistance to Solvents Reference MIL-STD-202 Method 215	Appearance : no damage. Inductance : within±10% of initial value. RDC : within±15% of initial value and shall not exceed the specification value.	Add an Aqueous wash chemical and follow chemical manufacturer's recommended parameters (i.e. solution temperature and immersion time). Applicable to ink marked components and not laser marked components																																														
8.10	Mechanical Shock Reference MIL-STD-202 Method 213		<table><tr><th>Type</th><th>Peak value (g's)</th><th>Normal duration (D) (ms)</th><th>Wave form</th><th>Velocity change (Vi)ft/sec</th></tr><tr><td>SMD</td><td>100</td><td>6</td><td>Half-sine</td><td>12.3</td></tr><tr><td>THT</td><td>100</td><td>6</td><td>Half-sine</td><td>12.3</td></tr></table> 3 shocks in each direction along 3 perpendicular axes. (18 shocks).	Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec	SMD	100	6	Half-sine	12.3	THT	100	6	Half-sine	12.3																															
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SMD	100	6	Half-sine	12.3																																													
THT	100	6	Half-sine	12.3																																													
8.11	Vibration Reference MIL-STD-202 Method 204	Oscillation Frequency : 10Hz~2kHz~10Hz for 20 minutes Equipment : Vibration checker Total Amplitude : 5g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations)																																															
8.12	Resistance to Soldering Heat Reference MIL-STD-202 Method 210		Test condition : THT : Conditions B or C <table><tr><th>Solder technique simulation</th><th>Test condition</th><th>Temperature (°C)</th><th>Time(s)</th><th>Temperature ramp/immersion and emersion rate</th><th>Number of heat cycles</th></tr><tr><td>Dip</td><td>B</td><td>260 ±5 (solder temp)</td><td>10±1</td><td>25mm/s ±6mm/s</td><td>1</td></tr><tr><td>Wave : Topside board-mou nt product</td><td>C</td><td>260 ±5 (solder temp)</td><td>20±1</td><td></td><td>1</td></tr></table> Depth : completely cover the termination SMD : Condition K, time above 217° C, 60s-150s, Number of heat cycles : 3 Continental <table><tr><th>Component Size</th><th>Ramp up to 150°C</th><th>Tsmin</th><th>Ts</th><th>Tsmax</th><th>TL</th><th>tL</th><th>Tpeak*</th><th>Tp**</th><th>Time 25°C to peak</th><th>Ramp down</th></tr><tr><td>Thickness < 1.6mm or Thickness 1.6mm-2.5mm and Volume < 350mm³</td><td rowspan="3">3.0±0.1°C/s (The component shall be specified for usage in serial production with up to 3.0°C/s)</td><td rowspan="3">≥ 190°C</td><td rowspan="3">≥ 110s</td><td rowspan="3">≥ 200°C</td><td rowspan="3">≥ 217°C</td><td rowspan="3">≥ 90s</td><td>≥ 260°C</td><td>≥ 40s</td><td rowspan="3">≥ 300s</td><td rowspan="3">6.0±0.1°C/s (The component shall be specified for usage in serial production with up to 6.0°C/s)</td></tr><tr><td>Thickness 1.6mm-2.5mm and Volume < 350-2000mm³ or Thickness > 2.5mm and Volume < 350mm³</td><td>≥ 250°C</td><td>≥ 30s</td></tr><tr><td>Thickness 1.6mm-2.5mm and Volume > 2000mm³ or Thickness > 2.5mm and Volume > 350mm³</td><td>≥ 245°C</td><td></td></tr></table> Table 1 : Minimum requirements for lead-free soldering *peak temperature is measured on the centre top of the component package **tp measured @ T peak-5°C	Solder technique simulation	Test condition	Temperature (°C)	Time(s)	Temperature ramp/immersion and emersion rate	Number of heat cycles	Dip	B	260 ±5 (solder temp)	10±1	25mm/s ±6mm/s	1	Wave : Topside board-mou nt product	C	260 ±5 (solder temp)	20±1		1	Component Size	Ramp up to 150°C	Tsmin	Ts	Tsmax	TL	tL	Tpeak*	Tp**	Time 25°C to peak	Ramp down	Thickness < 1.6mm or Thickness 1.6mm-2.5mm and Volume < 350mm ³	3.0±0.1°C/s (The component shall be specified for usage in serial production with up to 3.0°C/s)	≥ 190°C	≥ 110s	≥ 200°C	≥ 217°C	≥ 90s	≥ 260°C	≥ 40s	≥ 300s	6.0±0.1°C/s (The component shall be specified for usage in serial production with up to 6.0°C/s)	Thickness 1.6mm-2.5mm and Volume < 350-2000mm ³ or Thickness > 2.5mm and Volume < 350mm ³	≥ 250°C	≥ 30s	Thickness 1.6mm-2.5mm and Volume > 2000mm ³ or Thickness > 2.5mm and Volume > 350mm ³	≥ 245°C	
Solder technique simulation	Test condition	Temperature (°C)	Time(s)	Temperature ramp/immersion and emersion rate	Number of heat cycles																																												
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Thickness 1.6mm-2.5mm and Volume > 2000mm ³ or Thickness > 2.5mm and Volume > 350mm ³							≥ 245°C																																										
8.13	ESD Reference AEC-Q200-002		Direct Contact and Air Discharge PASSIVE COMPONENT HBM ESD Discharge Waveform to a Coaxial Target Test method : AEC-Q200-002 Test mode : Contact Discharge Discharge level : 4 KV (Level : 2)																																														

Item		Performance	Test Condition																																
8.14	Solderability Reference J-STD-002	More than 95% of the terminal electrode should be covered with solder.	Through-hole Technology (THT : Method A1, Coating Durability Category 2) • SMD : Method B1, Coating Durability Category 2 Method D, Coating Durability Category 2 • Magnification 50x • Pre and Post Electrical Test not required. • Non-soldered type mounting/attach are not applicable. <table><tr><th>Reference</th><th>Method A1</th><th>Method B1</th><th>Method D</th></tr><tr><td>Welding Process</td><td>Reflow Soldering</td><td>Reflow Soldering for Other Components</td><td>Lead-free Soldering</td></tr><tr><td>Type of Solder</td><td>Tin-Silver-Copper Solder</td><td>Tin-Silver-Copper Solder</td><td>Tin-Silver-Copper Solder</td></tr><tr><td>Flux Immersion Time</td><td>5-10s</td><td>5-10s</td><td>5-10s</td></tr><tr><td>Immersion Angle</td><td>20°~45°</td><td>20°~45°</td><td>20°~45°</td></tr><tr><td>Solder Temperature</td><td>245±5° C</td><td>245±5° C</td><td>260±5° C</td></tr><tr><td>Solder Immersion Time</td><td>5+0/-0.5s</td><td>5+0/-0.5s</td><td>30+5/-0s</td></tr><tr><td>Speed of Immersion and Withdrawal</td><td>25±6mm/s</td><td>25±6mm/s</td><td>25±6mm/s</td></tr></table>	Reference	Method A1	Method B1	Method D	Welding Process	Reflow Soldering	Reflow Soldering for Other Components	Lead-free Soldering	Type of Solder	Tin-Silver-Copper Solder	Tin-Silver-Copper Solder	Tin-Silver-Copper Solder	Flux Immersion Time	5-10s	5-10s	5-10s	Immersion Angle	20°~45°	20°~45°	20°~45°	Solder Temperature	245±5° C	245±5° C	260±5° C	Solder Immersion Time	5+0/-0.5s	5+0/-0.5s	30+5/-0s	Speed of Immersion and Withdrawal	25±6mm/s	25±6mm/s	25±6mm/s
Reference	Method A1	Method B1	Method D																																
Welding Process	Reflow Soldering	Reflow Soldering for Other Components	Lead-free Soldering																																
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Speed of Immersion and Withdrawal	25±6mm/s	25±6mm/s	25±6mm/s																																
8.15	Electrical Characterization	Refer Specification for Approval.	Parametrically test per lot and sample size requirements, summary to show Min, Max, Mean and Standard deviation at room as well as Min and Max operating temperatures. Pre and Post Electrical Test not required																																
8.16	Flammability	In accordance with Referenced Standards.	Reference UL-94 or IEC 60695-11-5																																
8.17	Board Flex(SMD) Reference AEC-Q200-005	Appearance : no damage. Inductance : within±10% of initial value. RDC : within±15% of initial value and shall not exceed the specification value.	Preconditioning : run through reflow for 3 times. (IPC/JEDEC J-STD-020F Classification Reflow Profiles) Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. 																																
8.18	Terminal Strength(SMD) Reference AEC-Q200-006		With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. 																																

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

9. Soldering Specifications

(1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020F)

(3) Iron Reflow:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.(Fig. 2)

- Preheat circuit and products to 150°C
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 355°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.

Fig.1 Soldering Reflow

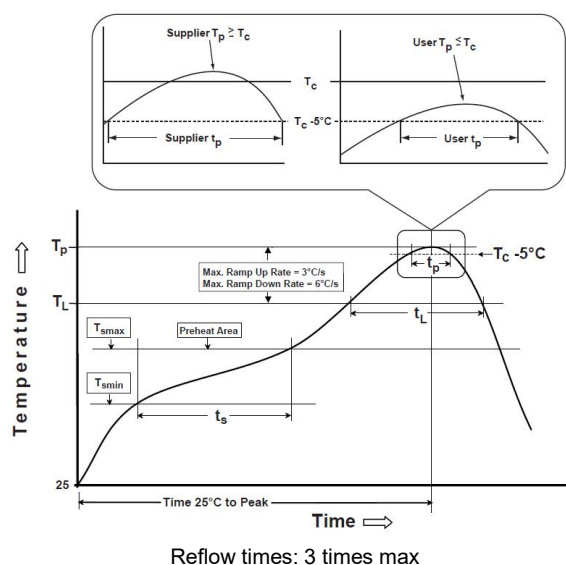
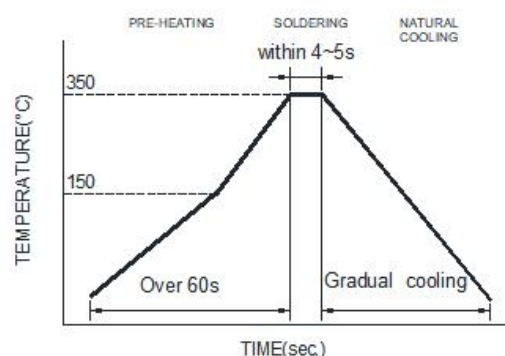


Fig.2 Iron soldering temperature profiles



Iron Soldering times: 1 times max.

Soldering iron Method : 350± 5°C max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min(T_{smin})	150°C
-Temperature Max(T_{smax})	200°C
-Time(t_s)from(T_{smin} to T_{smax})	60-120seconds
Ramp-up rate(T_L to T_p)	3°C/second max.
Liquidus temperature(T_L)	217°C
Time(t_L)maintained above T_L	60-150 seconds
Classification temperature(T_c)	See Table (1.2)
Time(t_p) at $T_c - 5^\circ\text{C}$ (T_p should be equal to or less than T_c .)	* < 30 seconds
Ramp-down rate(T_p to T_L)	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

T_p : maximum peak package body temperature, T_c : the classification temperature.

For user (customer) T_p should be equal to or less than T_c .

* Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020F.

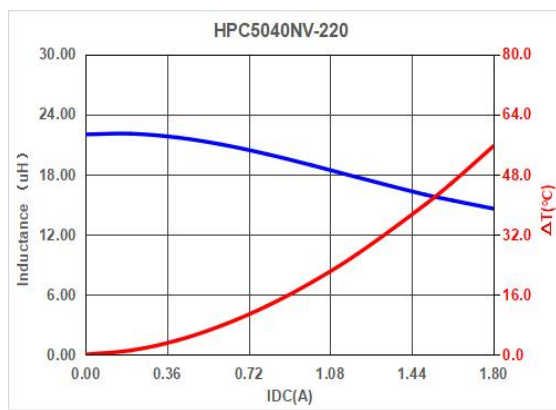
10. Notes

- (1) When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method, and dry it off immediately.
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappears.
- (9) The high power ultrasonic washing may damage the choke body.
- (10) Before use, the user should determine whether this product is suitable for their own design. Our company only guarantees that the product meets the requirements of this specification.

Application Notice

- Storage Conditions
To maintain the solderability of terminal electrodes:
 1. TAI-TECH products meet IPC/JEDEC J-STD-020F standard-MSL, level 1.
 2. Temperature and humidity conditions: Less than 40°C and 85% RH.
 3. Recommended products should be used within 12 months from the time of delivery.
 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

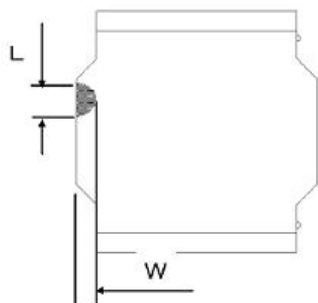
11. Typical Performance Curves



12. Appearance criterion

12-1. Core chipping

The appearance standard of the chipping size on top side, and bottom side ferrite core is listed below.
 Chip off is generated during molding and manufacturing process.
 Chip off acceptance limits subjected to the product size.
 Our current Defect limit is based on the IPC-A-610.
 Some chip off does not impact the product function, see the IPC standard 1 & 2.

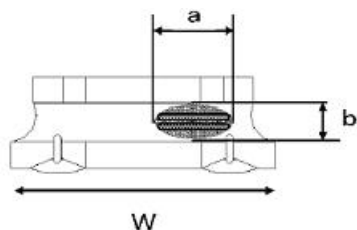


L	≤50 % of the length
W	≤25 % of the width

Defects usually occur at the corners and edges of the product, There will be a slight defect black and rough, but not exposed copper, and does not affect the product performance and reliability.

12-2. Void appearance tolerance Limit

Size of voids occurring to coating resin is specified below.



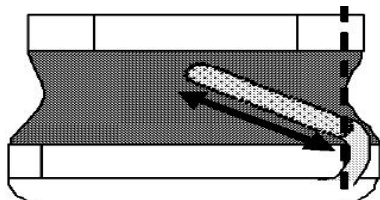
Exposed wire tolerance limit of coating resin part on product side.

Size of exposed wire occurring to coating resin is specified below.

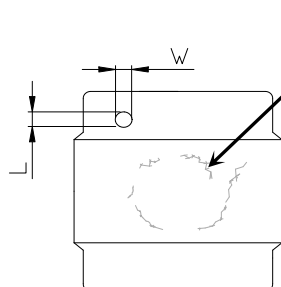
1. Width direction (dimension a): Acceptable when $a \leq w/2$.
2. Length direction (dimension b): Dimension b is not specified.
3. The total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, and is acceptable.

12-3. External appearance criterion for exposed wire

Exposed winding wire at the secondary side is regarded as qualified product.



12-4. Electrode appearance criterion for exposed wire



Visual check on core surface with no crack means pass.

Only top side of wire is exposed.
 (regardless of whole top side of wire exposed)

Conforming

Wire is soldered insufficiently and less than half of outer diameter is covered with solder.
 Less than 1/2 of joint side length.
 (More than 1/2 is selected as defect)

L&w
≤20% of the area on one single pad

Foreign materials on the product body is inevitable and accepted.
 Electrodes with foreign body (dirt) appearance standards
 Foreign materials (dirt) will not affect the coplanarity of PAD,
 below the example of foreign materials (dirt) quantity ≤2PCS on single PAD.
 Dimensions range as shown in the table.